10630537_CLSTITLES.txt Titles of Most Frequently Occurring Classifications of Patents Returned From A Search of 10630537 on November 16, 2005 (19 OR, 7 XR) 714/726 714: Class ERROR DETECTION/CORRECTION AND FAULT **DETECTION/RECOVERY** PULSE OR DATA ERROR HANDLING .Digital logic testing 714/699 714/724 714/726 ...Scan path testing (e.g., level sensitive scan design (LSSD)) 12 714/727 (6 OR, 6 XR)Class 714 : ERROR DETECTION/CORRECTION AND FAULT **DETECTION/RECOVERY** PULSE OR DATA ERROR HANDLING .Digital logic testing ..Scan path testing (e.g., level sensitive scan design (LSSD)) 714/699 714/724 714/726 714/727 ...Boundary scan (7 OR, 3 XR) 10 714/30 Class 714 : ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY 714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING 714/1 .Reliability and availability 714/25 .. Fault locating (i.e., diagnosis or testing) 714/27 ...Particular access structure 714/30Built-in hardware for diagnosing or testing within-system component (e.g., microprocessor test mode circuit, scan path) 714/724 (3 OR, 3 XR)class 714 : ERROR DETECTION/CORRECTION AND FAULT **DETECTION/RECOVERY** PULSE OR DATA ERROR HANDLING .Digital logic testing 714/699 714/724 714/729 (1 OR, 5 XR)714 : ERROR DETECTION/CORRECTION AND FAULT Class **DETECTION/RECOVERY** PULSE OR DATA ERROR HANDLING .Digital logic testing 714/699 714/724 ..Scan path testing (e.g., level sensitive scan design (LSSD)) 714/726 714/729 ...Plural scan paths (1 OR, 4 XR) 324/158.1 324 : ELECTRICITY: MEASURING AND TESTING 324/158.1 **MISCELLANEOUS** 365/201 (0 OR, 5 XR)365 : STATIC INFORMATION STORAGE AND RETRIEVAL 365/189.01 READ/WRITE CIRCUIT

714 : ERROR DETECTION/CORRECTION AND FAULT

PULSE OR DATA ERROR HANDLING .Digital logic testing

DETECTION/RECOVERY

365/201

Class

714/699

714/724

714/733

.Testing

(0 OR, 5 XR)

```
10630537_CLSTITLES.txt
         714/733
                         ..Built-in testing circuit (BILBO)
  714/731
                   (2 OR, 2 XR)
         Class
                  714 : ERROR DETECTION/CORRECTION AND FAULT
                           DETECTION/RECOVERY
         714/699
                         PULSE OR DATA ERROR HANDLING
         714/724
                         .Digital logic testing
                         ...Scan path testing (e.g., level sensitive scan design (LSSD))
...Clock or synchronization
         714/726
         714/731
   716/4
                    (0 \text{ OR}, 4 \text{ XR})
         Class
                          DATA PROCESSING: DESIGN AND ANALYSIS OF
                            CIRCUIT OR SEMICONDUCTOR MASK
         716/1
                         CIRCUIT DESIGN
         716/4
                         .Testing or evaluating
                  (0 OR, 3 XR)
324 : ELECTRICITY: MEASURING AND TESTING
   324/73.1
         Class
         324/73.1
                         PLURAL, AUTOMATICALLY SEQUENTIAL TESTS
   714/732
                   (1 OR, 2 XR)
         Class
                  714 : ERROR DETECTION/CORRECTION AND FAULT
                            DETECTION/RECOVERY
                         PULSE OR DATA ERROR HANDLING .Digital logic testing
         714/699
         714/724
         714/732
                         ...Signature analysis
  714/33
                    (1 OR, 1 XR)
                  714:
         Class
                          ERROR DETECTION/CORRECTION AND FAULT
                            DETECTION/RECOVERY
         714/100
                         DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING
         714/1
                         .Reliability and availability
         714/25
                         .. Fault locating (i.e., diagnosis or testing)
                         ...Particular stimulus creation
         714/32
         714/33
                         ....Derived from analysis (e.g., of a
                             specification or by stimulation)
  714/718
                   (2 OR, 0 XR)
                  714 : ERROR DETECTION/CORRECTION AND FAULT
         Class
                           DETECTION/RECOVERY
         714/699
714/718
                         PULSE OR DATA ERROR HANDLING
                         .Memory testing
                   (0 OR, 2 XR)
  714/725
                  714 : ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
         Class
         714/699
                         PULSE OR DATA ERROR HANDLING
                         .Digital logic testing
..Programmable logic array (PLA) testing
         714/724
         714/725
2 714/734
                   (0 \text{ or, } 2 \text{ xr})
                  714 : ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
         Class
                         PULSE OR DATA ERROR HANDLING .Digital logic testing
         714/699
         714/724
         714/734
                         ..Structural (in-circuit test)
                  (0 OR, 2 XR)
714 : ERROR DETECTION/CORRECTION AND FAULT
2 714/736
         Class
                           DETECTION/RECOVERY
```

	10630537_CLSTITLES.txt
714/699	PULSE OR DATA ERROR HANDLING
714/724	.Digital logic testing
714/736	Device response compared to expected
	fault-free response